

SM2258 L06B F/W Release Note

FW Package	MP Tool Version	ISP Version	Note
Q1018CD	Q1016A	Q1018CD	● Add damage fast boot for SPOR. ● Optimize fill dummy. ● Modify GC policy to avoid no free block issue. ● Modify BGC flow to enhance performance ● Fix read link and Act 1 ECC fail issue.
Q0710B	Q0718A	Q0710B	● Add 3 Channel support ● Modify for potential SSC disable issue ● Fix LTS result issue in RDT FW
Q0525A	Q0525A	Q0525A	● Fix ID table Word 69 ● Fix Sanitize issue ● Add 0xFD command for DevSLP function ● Fix download ucode PIO mode can't return status issue ● Modify DST feature flow ● Enhance SLC wear-leveling policy
Q0330C	Q0406B	Q0330C	● Support TCG function (enable by MP option). Industrial grade only. ● Fix Security Erase flag non-update issue at WPRO meta-data. ● Add bypass 1~3 bit ECC Adj. fail on Pretest ● Fix EOB not complete issue ● Fix false power on count when DevSLP ● Modify DRAM VDT voltage ● Add thermal throttling function ● Fix erase count fall back issue ● Record retry pass count and maximum hard decode count in RDT flow
Q0125A	Q0125A	Q0125A	● Fix all block 0 is bad issue. ● Fix DevSlp issue at Intel flash. ● Fix under 2TB out of range problem ● Optimize PowerOnHour count ● Fix DevSlp when HIPM and DIPM not enabled ● Force Gen2 or Gen1 , set in MP tool ● Modify ID table W77[2:0] to fix Ulink SATA compliance fail issue. ● FW abort 28bit r/w cmd if LBA = 0xFFFFFFFF. ● Fix WPRO issue.
P1026A	P1026A	P1026A	● Modify for cache program performance. ● Fix WPRO lost issue ● Fix ECC fail buffer flag issue. ● Fix SLC erase count issue. ● Fix 2 Die DRAM issue. ● Add polling state for erase command. ● Add DDR300 setting. ● Add Setting SSC range from CID option. ● Add Erase delay time setting from CID ● Add Mira DRAM setting
P0818A	P0817A	P0818A	● Bug fix of 1TB burn-in issue (parameter overflow). ● Correct ID table. ● Enable 2TB support.

P0802A	P0729A	P0802A	● Bug fix and enhancement of RDT function. ● Bug fix of SPOR issue. ● Enable forced SLC FW algorithm option (with this feature, L06B LPP is not required).
P0714B	P0714A	P0714B	● Initial enablement of Intel L06B.
P0615A	P0329A	P0615A	● Initial enablement of Micron L06B.

Note:

1. F/W and ISP update is recommended.
2. History # is denoted by “Version-Date”.

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